



B  
Image

Docket No.: M4065.0340/P340  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Graham Kirsch

Application No.: 09/652,003

Confirmation Number: 2935

Filed: August 31, 2000

Art Unit: 2186

For: METHOD AND APPARATUS FOR  
CONNECTING A MASSIVELY PARALLEL  
PROCESSOR ARRAY TO A MEMORY  
ARRAY IN A BIT SERIAL MANNER

Examiner: W. H. Choi

SUBMISSION OF DRAWINGS

MS PGPUB Drawings  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Notice Regarding Drawings dated May 4, 2005, submitted herewith is one set (seven sheets, eight figures) of drawings for filing in the above-identified patent application. Kindly substitute the enclosed drawings for the drawings submitted with the originally filed application.

Dated: May 25, 2005

Respectfully submitted,

By \_\_\_\_\_

Thomas J. D'Amico

Registration No.: 28,371

Christopher S. Chow

Registration No.: 46,493

DICKSTEIN SHAPIRO MORIN &  
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant